

Fig. 1 (Prior Art)

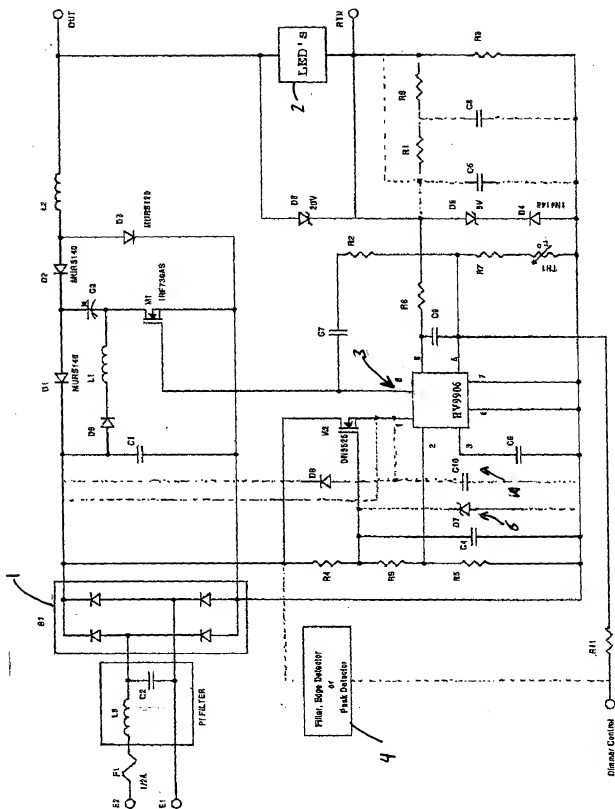


Fig. 2

Functional Block Diagram

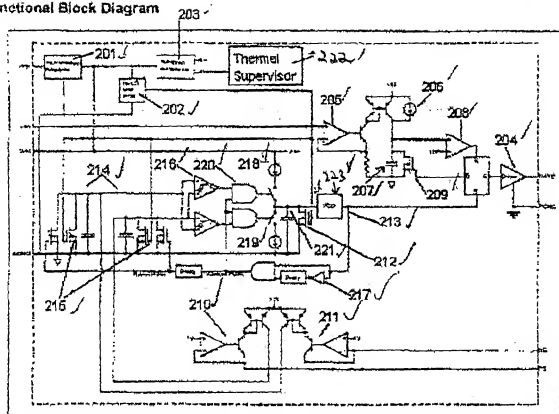


Fig. 3